



09/585682

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kei-Yu Ko et al.

Examiner: Chris C Chu

Patent No.: 7,173,339

Group Art Unit: 2815

Issue Date: February 6, 2007

Docket No: 2269-3526.2US

Title: SEMICONDUCTOR DEVICE HAVING A SUBSTRATE AN UNDOPE SILICON OXIDE  
STRUCTURE AND AN OVERLAYING DOPED SILICON OXIDE STRUCTURE WITH A  
SIDEWALL TERMINATING AT THE UNDOPE SILICON OXIDE STRUCTURE

Commissioner for Patents  
Office of Patent Publication  
ATTN: Certificate of Correction Branch

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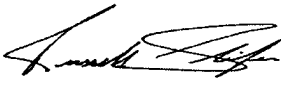
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- ☒ Request for Certificate of Correction.
- ☒ Certificate of Correction Form - PTO-1050 (in duplicate)
- ☒ A return postcard.

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Respectfully submitted,

Date: November 29, 2007

By: \_\_\_\_\_

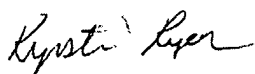
  
Russ Slifer  
Reg. No. 39,838

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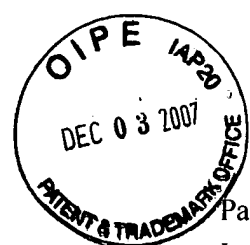
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Kyrstin Ryan

\_\_\_\_\_  
Name

  
\_\_\_\_\_  
Signature

DEC 6 2007



**IN UNITED STATES PATENT AND TRADEMARK OFFICE**

Patent No.: 7,173,339

Docket No: 2269-3526.2US

Issue Date: February 6, 2007

Patentee: Kei-Yu Ko et al.

Title: SEMICONDUCTOR DEVICE HAVING A SUBSTRATE AN UNDOPED SILICON  
OXIDE STRUCTURE AND AN OVERLAYING DOPED SILICON OXIDE  
STRUCTURE WITH A SIDEWALL TERMINATING AT THE UNDOPED SILICON  
OXIDE STRUCTURE

**REQUEST FOR CERTIFICATION OF CORRECTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
ATTN: Certificate of Correction Branch

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

**Pursuant to 1.20(a), the examiner is authorized to charge the Certificate of Correction fee of \$100.00 to the Deposit Account No. 13-3092.**

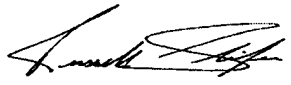
Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

Respectfully submitted,

**Customer Number: 26809**

Date: November 29, 2007

By: \_\_\_\_\_

  
Russ Slifer  
Reg. No. 39,838

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Kyrstin Ryan

\_\_\_\_\_  
Name

  
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## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,173,339

Page (1) of 1

DATED : February 6, 2007

INVENTOR(S) : Ko et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the face page, in field (54), in "Title", in column 1, line 2, after "SUBSTRATE" insert -- , --.

On the face page, in field (54), in "Title", in column 1, line 3, after "STRUCTURE" insert -- , --.

On the face page, in field (54), in "Title", in column 1, line 3, delete "OVERLAYING" and insert -- OVERLYING --, therefor.

In column 1, line 2, after "SUBSTRATE" insert -- , --.

In column 1, line 3, after "STRUCTURE" insert -- , --.

In column 1, line 3, delete "OVERLAYING" and insert -- OVERLYING --, therefor.

In column 8, line 7, in Claim 1, delete "surface;" and insert -- device region; --, therefor.

In column 8, line 9, in Claim 1, delete "surface," and insert -- device region, --, therefor.

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.  
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In column 1, line 2, after "SUBSTRATE" insert - - , - -.

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In column 8, line 7, in Claim 1, delete "surface;" and insert - - device region; - - , therefor.

In column 8, line 9, in Claim 1, delete "surface," and insert - - device region, - - , therefor.

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